

<b>Notice of Allowability</b>	Application No.	Applicant(s)
	09/870,447	BOLES ET AL.
	Examiner	Art Unit

Thomas J. Cleary

2111

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1.  This communication is responsive to 12 May 2005.
2.  The allowed claim(s) is/are 1-21.
3.  The drawings filed on 24 September 2001 are accepted by the Examiner.
4.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a)  All
  - b)  Some\*
  - c)  None
 of the:
  1.  Certified copies of the priority documents have been received.
  2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5.  A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6.  CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a)  including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1)  hereto or 2)  to Paper No./Mail Date \_\_\_\_\_.
  - (b)  including changes required by the attached Examiner's Amendment / Comment or in the Office action of
 Paper No./Mail Date \_\_\_\_\_.
7.  DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

#### Attachment(s)

1.  Notice of References Cited (PTO-892)
2.  Notice of Draftsperson's Patent Drawing Review (PTO-948)
3.  Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date 20050316; 20050512
4.  Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5.  Notice of Informal Patent Application (PTO-152)
6.  Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7.  Examiner's Amendment/Comment
8.  Examiner's Statement of Reasons for Allowance
9.  Other \_\_\_\_\_.

**DETAILED ACTION**

***Information Disclosure Statement***

1. The information disclosure statements (IDS) submitted on 16 March 2005 and 12 May 2005 have been considered by the Examiner.
2. The information disclosure statement filed 16 March 2005 identifies submitted Japanese Patent Application JP 01037124 A as JP 01037424 A. For completeness of record, the Examiner has considered Japanese Patent Application JP 01037124 A and cited it on the attached form PTO-892.

***Allowable Subject Matter***

3. Claims 1-21 are allowed. The Examiner believes the references submitted on the information disclosure statements of 16 March 2005 and 12 May 2005 to be cumulative and does not believe that the references anticipate or suggest the invention as claimed. The Examiner could find neither prior art cited in its entirety nor motivation to combine the prior art to construct the invention as claimed. Should Applicants be aware of any portions of the references believed to be related to the patentability of the present application, they are reminded to notify the Examiner in accordance with 37 CFR 1.313 and MPEP §1308.

4. In reference to independent claim 1, the prior art of record fails to teach a method of processing an interrupt disable instruction included in a program instruction sequence, comprising: fetching an interrupt disable instruction from the sequence including an operand specifying a number of cycles for disabling interrupt processing; and executing the instruction. In reference to independent claim 14, the prior art of record fails to teach a processor including an interrupt disable instruction processing feature, comprising: a program memory for storing instructions including an interrupt disable instruction having an operand specifying a number corresponding to an interrupt disable duration; a register for storing the number; an instruction fetch/decode unit for fetching and decoding instructions, the instruction fetch/decode unit decoding the interrupt disable instruction and disabling the interrupt processing capability of the processor based on the number.

5. The most relevant prior art found by the Examiner is US Patent Number 6,084,880 to Bailey et al. ("Bailey"), US Patent Number 5,875,342 to Temple ("Temple-342"), US Patent Number 5,937,199 to Temple ("Temple-199"), US Patent Number 5,740,451 to Muraki et al. ("Muraki"), and Z80 Family Interrupt Structure ("Z80"). Bailey teaches disabling interrupts by setting a bit in an interrupt enable register for a period of time specified in a register (See Figures 7 and 8 and Column 15 Lines 14-41). Bailey does not teach fetching an interrupt disable instruction that includes an operand for specifying a number of cycles for disabling interrupt processing. Temple-342 and

Temple-199 both teach an instruction that disables interrupts for a period of time specified in a register. Temple-342 and Temple-199 do not teach that the period of time is specified by an operand included in the interrupt disable instruction (See Abstract). Temple-342 and Temple-199 further teach that the value representing the length of time that interrupts are disabled should be fixed by the system and not be made available to the user to ensure that the timeout period is not altered to an abnormally long period of time, which would undermine the utility of coupling the interrupt mask with a timeout counter (See Column 8 Lines 21-29). Muraki teaches a counter that measures the maximum amount of time that interrupts are disabled while a program is running (See Abstract and Column 2 Line 27 – Column 3 Line 24). Muraki does not teach fetching an interrupt disable instruction that includes an operand for specifying a number of cycles for disabling interrupt processing. Z80 teaches the use of an instruction to disable interrupts. However, Z80 requires the use of an enable interrupt instruction to enable interrupts following a disable interrupt instruction. Z80 does not provide for an operand in the instruction indicating the length of time that the interrupts are to be disabled. Thus, the prior art of record is not seen to teach or suggest the invention in the combination as claimed.

6. The Examiner has interpreted the claims in light of the enabling specification (See Figures 3 and 4 and Page 9 Line 19 – Page 13 Line 2) in view of Applicant's persuasive arguments in the response of 22 April 2004 at Pages 4-6, and therefore finds that the claims are in condition for allowance. The Examiner has further

determined that because Claims 1-13 recite the limitations of "fetching an interrupt disable instruction" and "executing the instruction", the claims are tangibly embodied and therefore meet the statutory requirements of 35 USC §101.

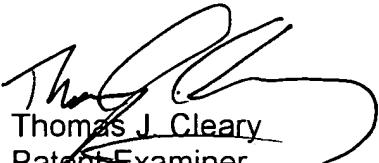
***Conclusion***

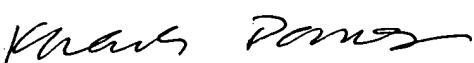
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thomas J. Cleary whose telephone number is 571-272-3624. The examiner can normally be reached on Monday-Thursday (7-3:30), Alt. Fridays (7-2:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark H. Rinehart can be reached on 571-272-3632. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TJC

  
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Art Unit 2111



Khanh Dang  
Primary Examiner